

CA3509M4-EV15

Evaluation Board

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Circuit Description

The CA3509M4-EV15 evaluation circuit board provides a quick and convenient means of evaluating the performance of CEL's GPS low noise amplifier CA3509M4 at frequency of 1.57GHz.

The PCB is FR4 four layer board. The top dielectric layer is 8mil thick and $Dk=4.3$. All signal traces are on the top metal layer and the second metal layer is ground. The rest is for mechanical support. The total board thickness is 62mil.

Typical Performance Specifications

Test Conditions:

$f=1575\text{MHz}$; $V_d=3\text{V}$, $I_d=15\text{mA}$

Noise Figure: 0.7dB (direct measurement on board). The board loss before the LNA is about 0.1dB

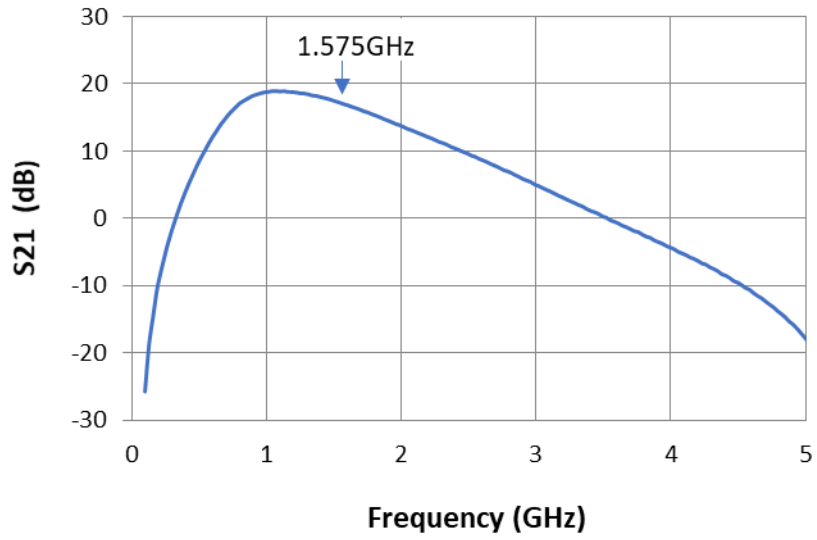
Gain: 17dB

Input return loss: 12dB

Output return loss: 12dB

IP1dB: -2dBm

Gain



Input and Output Return Loss

